**Verilog Lab 1 Requirement**

**Carry Select Adder (CSA)**

* Student 1:-

Name: عبدالرحمن حمدي احمد مخيمر

Section: 1

BN: 36

* Student 2:

Name: عبدالرحمن نعمان لقمان ابراهيم

Section: 2

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